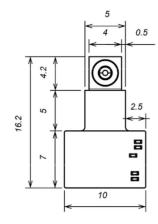
1/5" FORMAT 320 X 320-PIXEL EVENT-BASED METAVISION® SENSOR COMPACT OPTICAL MODULE WITH EMBEDDED FEATURES



GENX320 4X5



CenturyArks



PARAMETER	UNIT	SPECIFICATION	
Body dimension	mm	4 x 5 x 3.5	
Outline dimension	mm	10 x 17	
Connector		Hirose BM28 Series	
#Connector pins		30	
Control Interface		I ² C	
Data Interface		Parallel	
Lens Mount		Fixed lens position	
Aperture		f/3.1	
Focal Length		1.49 mm	
HFoV/VFoV	deg	65	
DFoV	deg	65	
IR filter		No	

FEATURES

- Prophesee GenX320 1/5" format 320x320 event-based vision sensor
- 4mm x 5mm flex-rigid module substrate
- 65° DFoV lens with fixed focus
- 30 pin board-to-board connector provides full sensor I/O breakout
- High-speed event data output (equivalent to >10kfps time resolution) with row-level 1µs-precision time stamping
- 0.05 lux Low light cutoff
- High dynamic range >140dB
- Ultra-low power mode 36µW
- Very-low operating power 3mW
- Embedded features: Anti-flicker filtering (AFK) + Event-Rate Controller (ERC) + Spatio-Temporal Contrast Filter (STC)
- Ambient Light Measurement
- ML-friendly compressed and uncompressed event data streams
- 8-bit parallel output interface I²C interface



APPLICATIONS

- AR/VR/XR
- Eye tracking
- Gesture recognition
- IoT
- Al on the Edge and Machine Learning
- Wearables



DESCRIPTION

Housed in a compact optical module with flex and Hirose board-to-board connector, the GenX320 - 4x5 offers a powerful solution for diverse embedded vision applications. At the heart of the 4x5 module is a 320x320 1/5" format 6.3µm pixel BSI stacked event-based Metavision® sensor. GenX320 was designed with the explicit goal to improve integrability and usability in at-the-edge vision systems. This includes event data pre-processing and formatting, data interface compatibility and low-latency connectivity to different processing platforms including latest low-power, neuromorphic processors. The sensor has been optimized for very low power operation, featuring a hierarchy of application-specific power modes. GenX320 contains an integrated Event Signal Processing functions. An Event Rate Controller (ERC) allows to cap the output event rate to a programmable limit. A Spatio-Temporal Contrast filter (STC) detects and removes redundant bursts and trails of events triggered by high contrast features in the scene. An Anti-Flicker (AFK) filter detects and filters events generated by flickering light sources in either band pass or band stop modes.

SENSOR LOW POWER MODES							
MODE	ULTRA LOW POWER	LOW POWER STANDBY	LOW POWER MONITOR	CPI STREAMING	CPI STREAMING	MIPI STREAMING	
Sub-system				100kEPS CPI @10MHz	1MEPS CPI @10MHz	10MEPS MIPI @800MHz	
Pixel array	3x3 GCD		320x320 pixels	320x320 pixels	320x320 pixels	320x320 pixels	
Digital ICN + CPU	Powered down	Powered, Clocked	Powered, Clocked	Powered, Clocked	Powered, Clocked	Powered, Clocked	
Digital readout	Powered down	Powered, Clocked	Powered, Clocked	Powered, Clocked	Powered, Clocked	Powered, Clocked	
Digital ESP + Output I/F	Powered down	Powered, Gated	Powered, Gated	Powered, Clocked	Powered, Clocked	Powered, Clocked	
	Total: 36µW	Total: 1.8mW	Total: 2.9mW	Total: 3mW	Total: 4.8mW	Total: 22.8mW	
	AUTONOMOUS FAST WAKEUP Ims						

ORDERING CODES AND BRIEF DESCRIPTION